
MLT-Satellite- Modules LTC6655

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The MLT-LTC6655 satellite module is an breakout board for the Linear's LTC6655 voltage reference family. The board support all LTC6655 in 8-lead MSOP packages.

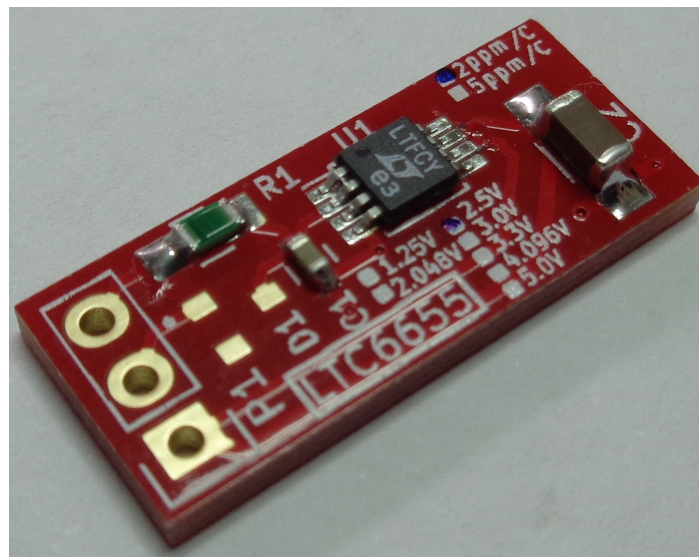


Figure 1: photo of the LTC6655 Module

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The License is attached at the end of this document as well as a Guide to the CERN OHL v.1.2.

Please feel free to give your highly appreciated feedback, comments and suggestions. Or ask any questions about the board. We'll be glad to answer or help you.

Specification

- A totally number of 14 different options available
- Board dimension: 21.717 mm x 15.144 mm (0.855" x 0.36")
- Vout+0.5V to 13.2V power supply range
- user extendable supply voltage range
- two layer board with total 1.6mm thickness.
- 100 mil 3 pin header, not soldered by default

Description

The user can extend the supply voltage range by adding a Zener diode (i.e. BZX84C12) and change R1 to an appropriate value. By default R1 is loaded with 0 Ohm and D1 is not mounted.

Available Options

The breakout board is available in totally 14 different options. There are 7 different output voltage options and each with two accuracy options.

Table 1: Options

Output Voltage	Initial Accuracy	Temperature Coefficient	Part used
1.250V	0.025%	2ppm/°C	LTC6655BHMS8-1.25
1.250V	0.05%	5ppm/°C	LTC6655CHMS8-1.25
2.048V	0.025%	2ppm/°C	LTC6655BHMS8-2.048
2.048V	0.05%	5ppm/°C	LTC6655CHMS8-2.048
2.500V	0.025%	2ppm/°C	LTC6655BHMS8-2.5
2.500V	0.05%	5ppm/°C	LTC6655CHMS8-2.5
3.000V	0.025%	2ppm/°C	LTC6655BHMS8-3.0
3.000V	0.05%	5ppm/°C	LTC6655CHMS8-3.0
3.300V	0.025%	2ppm/°C	LTC6655BHMS8-3.3
3.300V	0.05%	5ppm/°C	LTC6655CHMS8-3.3
4.096V	0.025%	2ppm/°C	LTC6655BHMS8-4.096
4.096V	0.05%	5ppm/°C	LTC6655CHMS8-4.096
5.000V	0.025%	2ppm/°C	LTC6655BHMS8-5
5.000V	0.05%	5ppm/°C	LTC6655CHMS8-5

Pin header P1

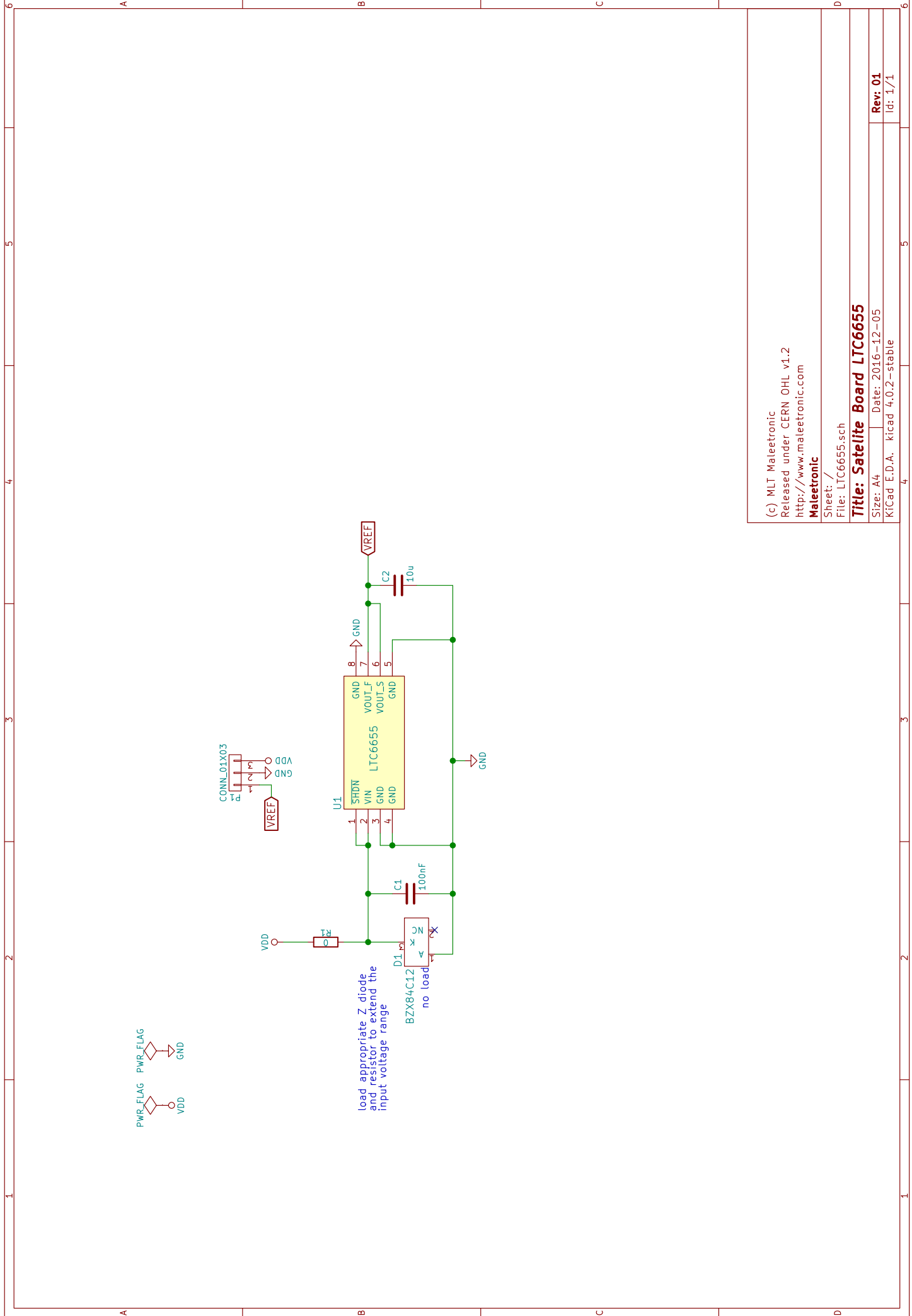
P1 is the pin header which comes as 100 mil(2.54mm) pin spacing. The header is not mounted to the board by default but straight headers will be shipped with the boards.

Table 2: Header P1

POSITION	SIGNAL
1	VREF
2	GND
3	VDD

Attachments

1. Board Schematics
2. CERN OHL v1.2
3. CERN OHL v1.2 How-to-Guide



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